

05-14-01

PATENT

A

Case Docket No. ADVA227.001AUS

Date: May 12, 2001

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jc860 U.S. PTO

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

ATTENTION: APPLICATION BRANCH

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): Tsuruto Matsui

For: PATTERN GENERATOR FOR SEMICONDUCTOR TEST SYSTEM

Enclosed are:

- (X) 7 sheet(s) of drawings.
- (X) 39 pages of specification, claims and abstract.
- (X) UNSIGNED declaration and power of attorney.
- (X) Return prepaid postcard.

## CLAIMS AS FILED

FOR	NUMBER FILED	NUMBER EXTRA	RATE	FEE
Basic Fee			\$710	\$710
Total Claims	8 - 20 =	0 x	\$18	\$0
Independent Claims	3 - 3 =	0 x	\$80	\$0
If application contains any multiple dependent claims(s), then add			\$270	\$0
TOTAL FILING FEE				\$710

- (X) A check in the amount of \$710 to cover the filing fee is enclosed.

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May 12, 2001

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**CERTIFICATE OF MAILING BY "EXPRESS MAIL"**

<b>Attorney Docket No.</b>	:	ADVA227.001AUS
<b>Applicant(s)</b>	:	Tsuruto Matsui
<b>For</b>	:	PATTERN GENERATOR FOR SEMICONDUCTOR TEST SYSTEM
<b>Attorney</b>	:	Yasuo Muramatsu
<b>"Express Mail"</b>		
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<b>Date of Deposit</b>	:	May 12, 2001

I hereby certify that the accompanying

Transmittal; a patent application in 39 pages of specification and 7 sheets of drawings; a check of \$710 for filing fee; unsigned declaration and power of attorney: Return Prepaid Postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

  
Yasuo Muramatsu